

Mattan Erez ^[1]

Temple Foundation Faculty Fellowship #4

Dr. Mattan Erez is an Associate Professor and holds the Temple Foundation Faculty Fellowship #4 in the Department of Electrical & Computer Engineering at The University of Texas at Austin.

He earned his Ph.D. in electrical engineering from Stanford University in 2007. He joined the faculty of The University of Texas at Austin in that same year. He has received several best paper awards at international conferences. Dr. Erez's research focus is computer architecture. Specifically, he is interested in the critical aspects of locality, parallelism and bandwidth constraints to overcome the limitations of today's architectures. One goal is to improve cooperation between the hardware, compiler and programmer in order to enable new levels of performance, efficiency and code-portability.

Dr. Erez's past research includes collaborative inter-departmental research on whole systems. He has also worked on processor architecture, system architecture, programming languages and applications. One current project seeks to use the abstractions in compiler technology to target specific low-level features of emerging and existing processors. This research may help meet power and energy constraints, provide reliable execution with low hardware costs, achieve performance goals in diverse environments, and manage the ever-growing complexity both in design and in verification.

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Support Staff:

[Leticia Lira](#) ^[3]

Research Interests:

Computer architecture
High-performance computing
Programming languages and models

Portrait:



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Faculty Position:

Associate Professor

Research Areas:

[Architecture, Computer Systems, and Embedded Systems \(ACSES\)](#) [5]
[Software Engineering and Systems](#) [6]

Graduate Studies Committee Member:

Yes

Chair:

No

User Account:

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Google Scholar:

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