Yale Patt Recognized with Four Papers Among Inaugural Micro Test of Time Award Winners

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Yale N. Patt[2], professor in the Department of Electrical and Computer Engineering at The University of Texas at Austin's Cockrell School of Engineering[3], has been recognized for his tremendous body of work in microarchitecture with four papers co-authored by he and his students selected as part of the ten award winners of the inaugural Micro Test of Time Award in 2014. The Micro Test of Time (ToT) Award recognizes the most influential papers published in past Micro conferences that have had significant impact in the field.

The award is sponsored by the IEEE Computer Society Technical Committee on Microprogramming & Microarchitecture[4] (TCuARCH). TCuARCH "addresses all aspects of microarchitecture including but not limited to high performance and low power processors (superscalar, VLIW, multithreading, multi-core, vector, etc.), memory hierarchies, compiler optimization, and instruction-level parallelism. The TC sponsors workshops, tutorials, and conferences, including the flagship International Symposium on Microarchitecture (MICRO)."

This first award recognizes the 10 most influential papers published during the first 25 years of the conference, between 1968 and 1992. They were selected from among all 544 papers published in Micro during that period. In the future, one paper will be selected each year from papers published between 18 and 22 years ago.

The 10 papers selected for this first year of the award are:[5]
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<th>Micro Year</th>
<th>Paper Title</th>
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Prof. Yale Patt holds the Ernest Cockrell, Jr. Centennial Chair in Engineering in the Cockrell School of Engineering at The University of Texas at Austin. He also holds the title of University Distinguished Teaching Professor [16]. He has received a number of awards for his research and teaching, most notably the highest honor in his specialty, computer architecture, the 1996 IEEE/ACM Eckert-Mauchly Award, "for important contributions to instruction level parallelism and superscalar processor design," and the highest honor in computer science education, the 2000 ACM Karl V. Karlstrom Outstanding Educator Award.

Among the other awards he has received are the 1995 IEEE Emanuell R. Piore Medal, the 1999 IEEE W.W. McDowell Award, the 2005 IEEE Charles Babbage Award, the 2011 IEEE B. Ramakrishna Rau Award [17], and the 2013 IEEE Computer Society Harry H. Goode Memorial Award [18].

Prof. Patt was elected to the National Academy of Engineering in 2014 [19], among the highest professional distinctions bestowed upon an engineer.
Keywords:

- Microarchitecture [20]

Related Faculty:

Yale Patt [2]

Related Research Areas:

Architecture, Computer Systems, and Embedded Systems (ACSES) [21]


Links